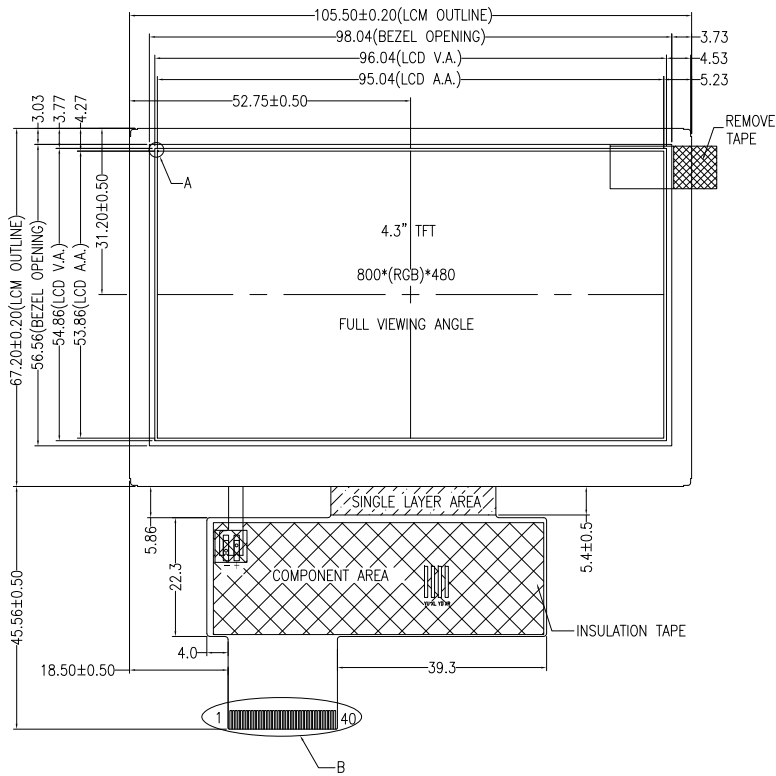
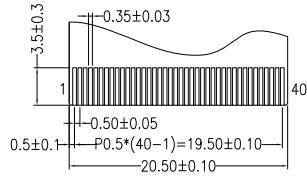
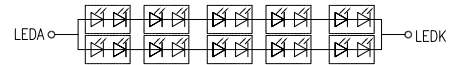
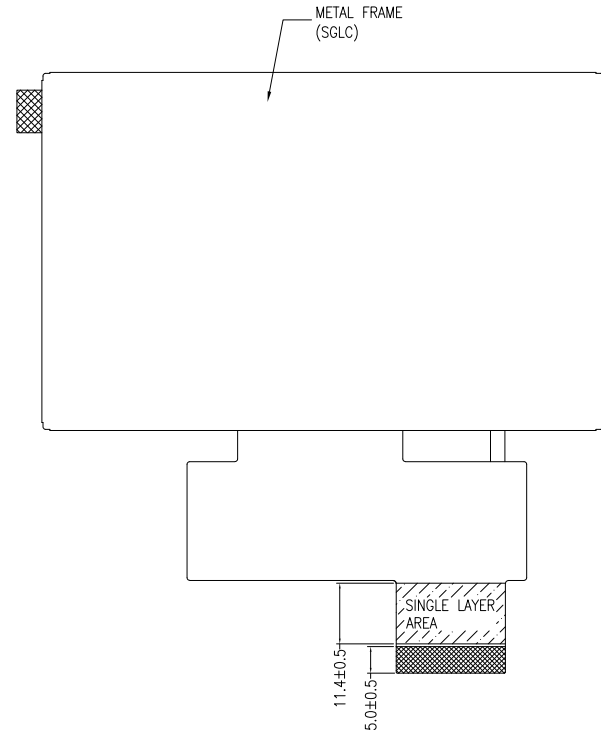
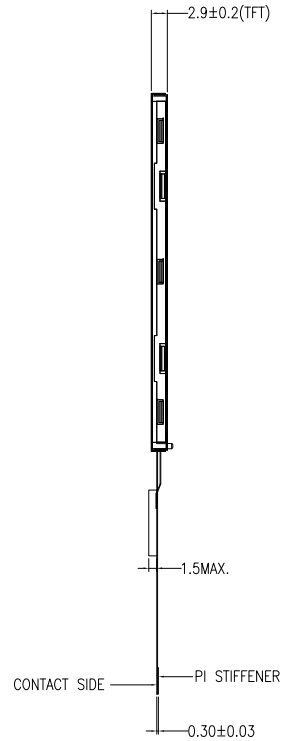


DETAIL A
(SCALE 100:1)



DETAIL B
(SCALE 4:1)



BACKLIGHT CIRCUIT DIAGRAM

PIN ASSIGNMENT	
PIN	SYMBOL
1	LEDK
2	LEDA
3	GND
4	VDD
5	R0
6	R1
7	R2
8	R3
9	R4
10	R5
11	R6
12	R7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
20	G7
21	B0
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	DCLK
31	DISP
32	HSYNC
33	VSYNC
34	DE
35	NC
36	GND
37	NC(YU)
38	NC(XL)
39	NC(YD)
40	NC(XR)

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VDD=3.3V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: ST7262E3
5. OPERATING TEMP.: -30°C ~ 85°C
6. STORAGE TEMP.: -30°C ~ 85°C
7. BACKLIGHT TYPE: WHITE DUAL-DICE LED, IF=40MA, VF=27V~33V
8. SURFACE LUMINANCE: 1000CD/M² (TYP.)
9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
10. RECOMMEND MATCHING CONNECTOR: HRS FH28-40S-0.5SH OR EQUIVALENT
11. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
12. UNMARKED TOLERANCE: ±0.3MM
13. ROHS COMPLIANT

MULTI-INNO TECHNOLOGY CO., LTD.					
DRAWN BY:	RITA	2025.02.28		MODULE P/N:	
CHECKED BY:			SCALE : 1/1	MI0430CFT-2	
APPROVED BY:			UNIT: mm	DESCRIPTION:	
VERSION NO:	01		SHEET NO.: 1/1	TFT MODULE	

01	FIRST ISSUE	RITA	2025.02.28
VER.	REVISED DESCRIPTION	REVISER	DATE